

INVENTOR INFORMATION

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APPLICATION INFORMATION

Title Line One:: Frame Synchronization Detecting Circuit  
Total Drawing Sheets:: 14  
Formal Drawings?:: Yes  
Application Type:: Utility  
Docket Number:: P/3236-25  
Secrecy Order in Parent Appl.?:: No

PRIOR FOREIGN APPLICATIONS

Foreign Application One:: 004581/2000  
Filing Date:: 01-13-2000  
Country:: Japan  
Priority Claimed:: Yes

Source:: PrintEFS Version 1.0.1